Serial No. 09/256,265



Docket No. 15917-0011

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kao et al. Serial No: 09/256,265 Filed: February 23, 1999 Docket No. 15917-0011 Group Art Unit: 2815 Examiner: G. Eckert, II

ر 6-15-9ء

For: "Method And Apparatus For Split Gate Source Side Injection Flash Memory Cell And Array With

Dedicated Erase Gates"

Examiner G. Eckert II Group No. 2815 Assistant Commissioner for Patents Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Restriction Requirement mailed May 10, 1999, Applicant provisionally elects with traverse the claims of Group I; namely, claims 1, 2, and 8-10, drawn to a semiconductor device, classified in class 257, subclass 315.

Applicants respectfully traverses the restriction requirement on the grounds that to be complete, a search for the subject matter of any of the alleged groups would require the same field of search as the other groups. Reconsideration of this requirement is requested.

Respectfully submitted,

Dated: June 9, 1999

Claude A.S. Hamrick Registration No. 22,586 Attorney for Applicant

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JUN 9 1999

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I hereby certify that this correspondence with all attachments is being sent by facsimile transmission to Examiner G. Eckert, II, Group Art Unit 2815, at (703) 308-7382, Assistant Commissioner for Patents, Washington, D.C. 20231 on June 9, 1999 by I Marie Kotsubo.

Date: June 9, 1999

I. Marie Kotsubo



FAX COVER LETTER

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: Serial No. 09/256,265, Filed February 23, 1999; Kao; "Method and Apparatus

For Split Gate Source Side Injection..."; Our Docket No. 15917-0011

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PLEASE RUSH TO EXAMINER ECKERT IMMEDIATELY

Completed by: M. Kotsubo / 3126

TO	COMPANY	PHONE NO.	FAX NO.
Examiner G. Eckert, II	U.S. Patent & Trademark Office; Group Art Unit No. 2815	703-305-2752	703-308-7382

Comments:

Dear Examiner Eckert: Please find enclosed a Response to Restriction Requirement for the above-identified matter. Please call me with any comments/questions. Regards,

Claude A.S. Hamrick

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